COMADC Board and Etc.

Jinyuan Wu For CKM Collaboration

The COMADC Board

- The COMADC board is a test board primarily to test the 2-phase current integrator (developed by Ken Nelson).
- It has the integrator, commercial 14-bit ADC, FPGA, Serdes and optic fiber out. (see comadc_sch.pdf for block diagram)

The COMADC Board

• The Serdes and optic fiber out are identical with the QIE test board (which Kwami designed). Therefore it can be plugged into the DAQ PCI module (which Bill designed) directly.

The COMADC Board

• A version of layout has been reached. (see "comadc_lay.pdf" pictures)

The COMADC Board (to do)

- Refine the layout. (0 Weeks?)
- Post process done by our CAD group. (2 weeks)
- Fabrication. (2 weeks)
- Partial assembly and tests.

The COMADC Board (schedule)

- Work req. date: 10-01-2002, Yes.
- Catch Jlab test beam?

Etc.

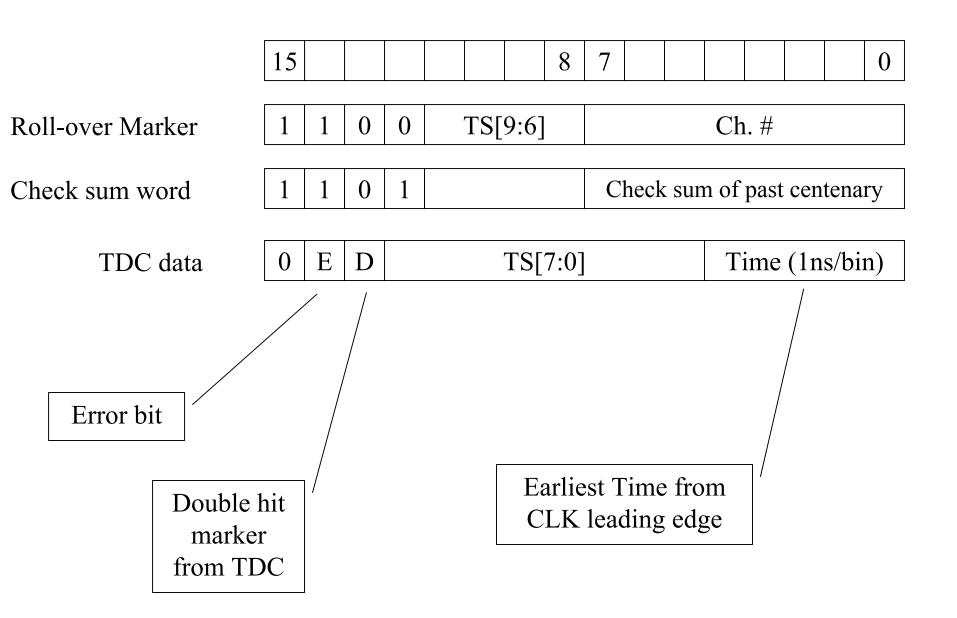
- What are those wires? (see comadc_lay.pdf) Too much details.
- But, the current technical choices in the digital design create some implications we should let the collaboration know.

This talk is too long.
Only fun part will be shown.
For full version, see CKM_62

Data Format for CKM

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TDC Data



Veto System Data

	15							8	7							0
Roll-over Marker	1	1	0	0	r	TS[9:6]					Ch	.#			
Hitlet Header	1	0	Е							,	TS[9:0]				
QIE only data	only data 0 0 Car				Cap	oID	QIE (2 range, 6 mantissa)									
(QIE+TDC) data	0	1	D	T	ime	(1n	s/bii	n)	Q	IE (2 ra	ınge	, 6 1	nan	tissa	a)
Check sum word	1	1	0	1					C	heck	sun	n of	past	cen	tena	ry

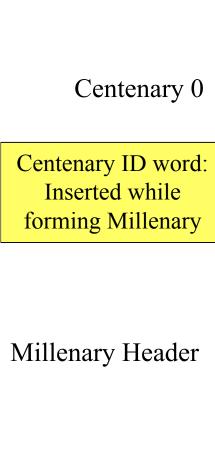
Centenaries: (TDC Example)

Check sum word		1	0	1		Check sur	n of past centenary			
	0	Е	D		TS[7:0]		Time (1ns/bin)			
	0	E	D		TS[7:0]		Time (1ns/bin)	C. 1		
TDC data	0	E	D		TS[7:0]		Time (1ns/bin)			
	0	Е	D		TS[7:0]		Time (1ns/bin)			
Roll-over Marker		1	0	0	TS[9:6]					
Check sum word	1	1	0	1		Check sur	n of past centenary			
	0	Е	D		TS[7:0]		Time (1ns/bin)			
	0	E	D		TS[7:0]		Time (1ns/bin)			
TDC data	0	E	D		TS[7:0]		Time (1ns/bin)			
	0	Е	D		TS[7:0]	Time (1ns/bin)				
	0	Е	D		TS[7:0]		Time (1ns/bin)			
Roll-over Marker	1	1	0	0	TS[9:6]		Ch. #			

Millenary Structure

Centenary 255

	1	1	0	1		Check sum of past centenary
	0	Е	D		TS[7:0]	Time (1ns/bin)
	1	1	0	0	TS[9:6]	Ch. #
	1	1	0	0		Centenary ID = TS[15:8]
_						



\prec		1	1	U	U	13[9.0]		CII. #					
		1	1	0	0		Centenar	y ID = TS[15:8]					
		1	1	0	1		Check sun	n of past centenary					
		0	Е	D		TS[7:0]		Time (1ns/bin)					
\prec) \	0	Е	D		TS[7:0]		Time (1ns/bin)					
		1	1	0	0	TS[9:6]		Ch. #					
$\overline{}$	_	1	1	0	0		Centenar	y ID = TS[15:8]					
		1	1	1	1	Millen	ary Check S	Sum [11:0]					
		1	1	1	1	Millena	ry Word Count [23:12]						
		1	1	1	1	Millena	Millenary Word Count [11:0]						
		1	1	1	1	Unified Channel Number [23:12]							
		1	1	1	1	Unified Channel Number [11:0]							
		1 1 1 Reso			1	Reset ID [3:0]	Millen	ary ID [31:24]					
		1	1	1	1	Mi	Millenary ID [23:12]						
		1	1	1	0	Millenary ID [11:0]							

Millenary Header Words

Centenary ID
One for each
centenary

15	8 7		0
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Optional

Redundant check information.
Inserted after

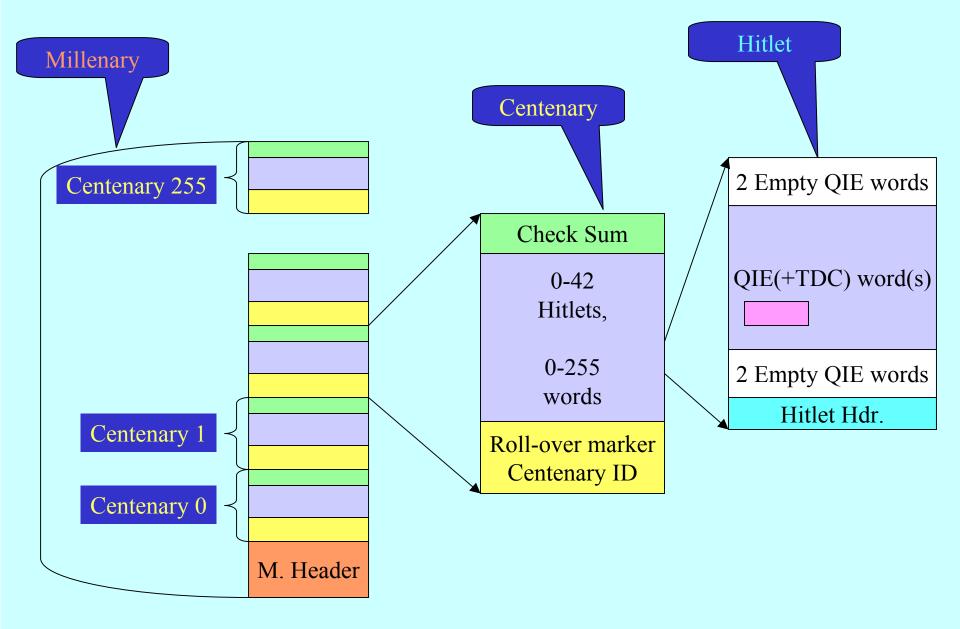
collecting the whole Millenary

The 2 nd header word	1
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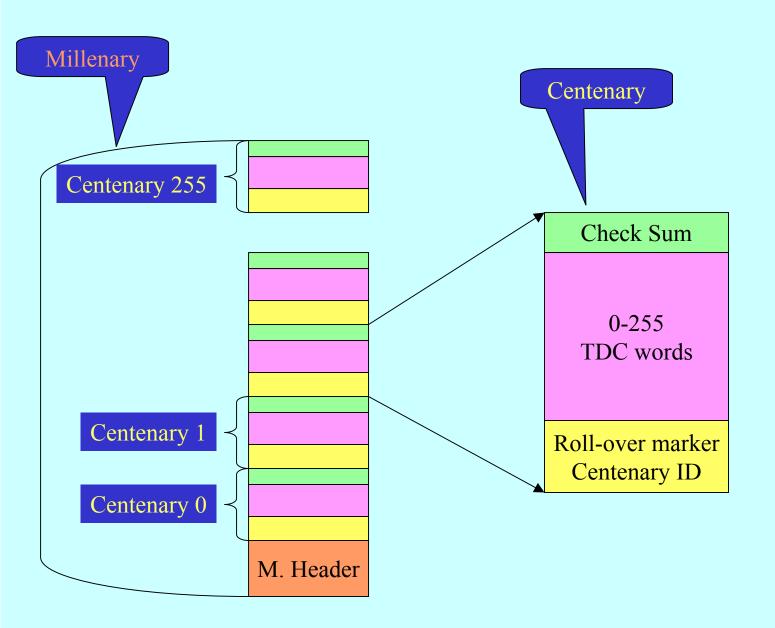
The 1st header word

	1	1	1	1	Millena	ry Check Sum [11:0]					
	1	1	1	1	Millenar	Millenary Word Count [23:12]					
)	1	1	1	1	Millena	Millenary Word Count [11:0]					
	1	1	1	1	Unified C	Unified Channel Number [23:12]					
	1	1	1	1	Unified C	Unified Channel Number [11:0]					
	1	1	1	1	Reset ID [3:0]	Millenary ID [31:24]					
	1	1	1	1	Millenary ID [23:12]						
	1	1	1	0	Millenary ID [11:0]						

(QIE+TDC) Data

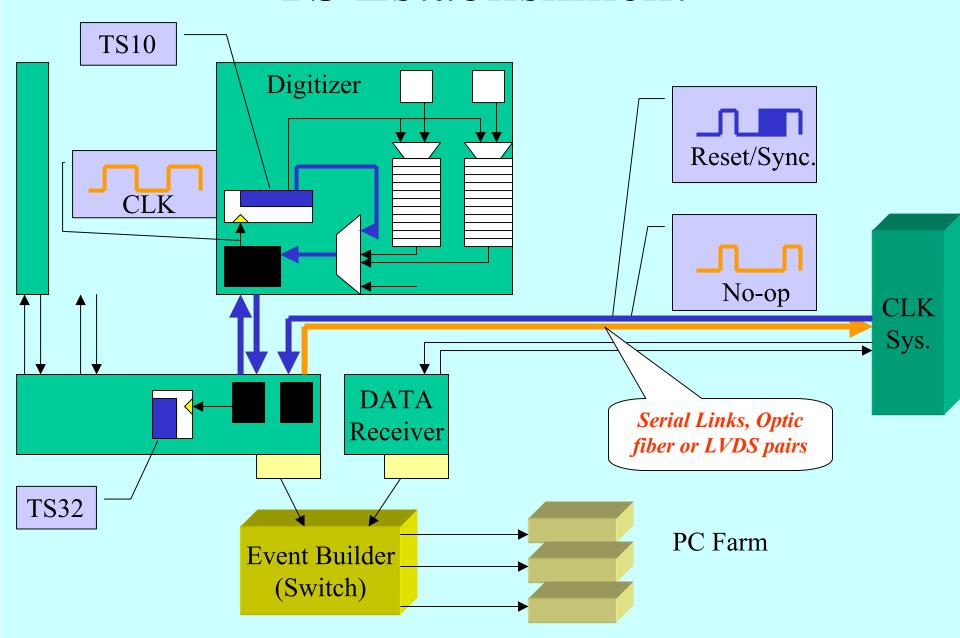


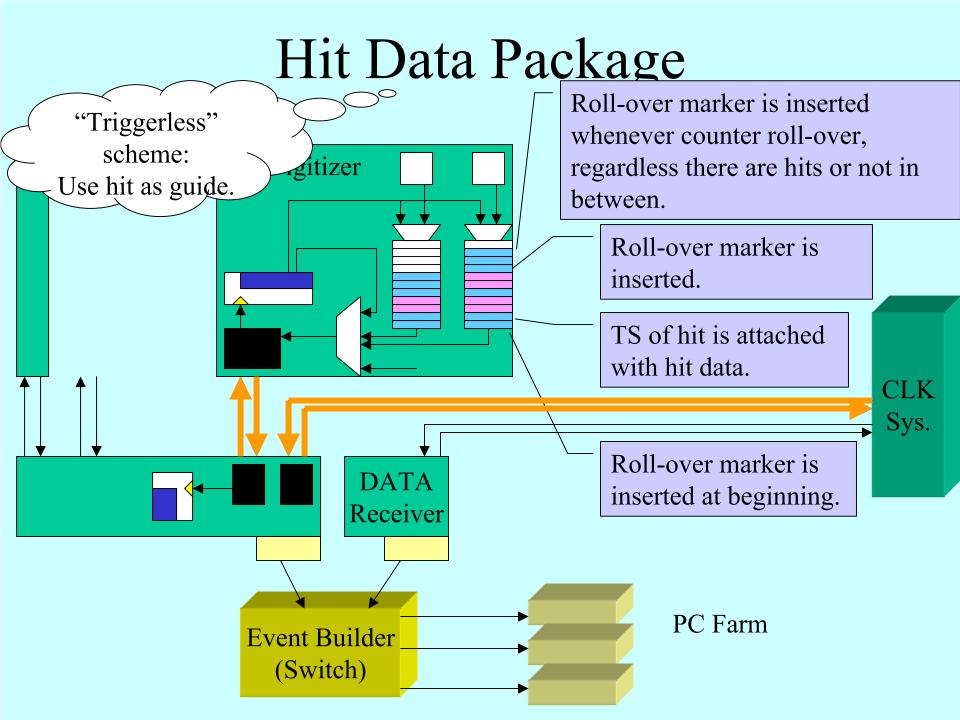
TDC Data



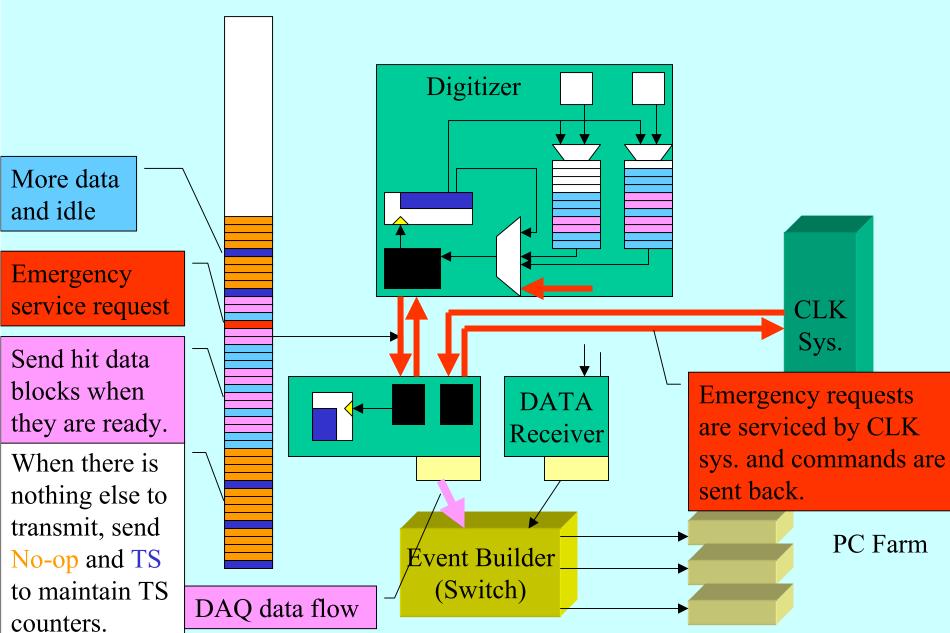
DAQ System Model

TS Establishment





Data Transmit format



The End

Thanks